3	L Number	Hits	Search Text	DB	Time stamp
adj3 register) and ((sav83 keep83 stor83)   Record\$3) near3 ((address adj3 extension)   (address adj		36	(address adj3 extension) and (extension		
(address adj3 value)) and (switch3]			adj3 register) and ((sav\$3 keep\$3 stor\$3	1 '	
(thread53 execution process processes   Value )   ((address adj3 extension) and (extension)   adj3 register) and ((sav33 keep33 stor33 record53)   near3 ((address adj3 extension)   (address adj3 extension)   (address adj3 extension)   (address adj3 extension)   near3 concatenat53)   ((address53 adj3 extension)   near3 concatenat53)   ((address52 adj3 extension)   near3 concatenat53)   ((address52 adj3 extension)   near3 concatenat53)   ((address52 adj3 extension)   near5 concatenat53)   ((address52 adj3 extension)   ((address52 adj3 extension)   (address adj3 extension)   ((address63)   (address63)   (addre					
Value    2				DERWENT;	
2				IBM_TDB	
adj3 register) and ((sav83 keep3 stor83 record83) near3 ((address adj3 extension) record83) near3 ((address adj3 extension) near3 (concatenat43)   USPAT; (value width))   USPAT; (value width)   USPAT; (value widt					
record\$3   near3 ((address adj3 extension) (address adj3 value)) and ((switch\$3   DRRWNT; [BM_TDB   2003/05/12 10:38   18M_TDB   2003/05/12 10:38   2003/05/12 10:3	4	2		1	2003/05/12 10:33
(address adj3 value)) and (switch\$3 (thread\$3 execution processor processes value))) and ((address\$3 adj3 extension) near3 concatenat\$3) same (another second) near3 (concatenat\$3) same (another second) near3 (concatenat\$3) same (address near3 signal)					
(threads] execution process processes value)) and ((address3 adj3 extension) near3 concatenat\$3)   (address5 adj3 extension) near3 concatenat\$3)   (address5 adj3 extension) near3 concatenat\$3)   (address5 adj3 extension) near3 concatenat\$3)   (address\$3 adj3 extension) near3   (address\$2 adj3 extension) near5   (address\$3 adj3 extension) near5   (address\$3 adj3 extension) near5   (address\$3 adj3 extension) near5   (address\$3 extension) near5   (address\$3 execution process processes   (adj3 extension) near5   (adj3 extension					
value ) and ((address\$3 adj3 extension)   near3 concatenat\$3)   ((address\$3 adj3 extension) near3   USPAT;					
Concatenat\$3) same ((another second) near3   US-PGPUB; POPO; JPO; DERMENT; IBM TOB USPAT; US-PGPUB; POPO; JPO; JPO; JPO; DERMENT; IBM TOB USPAT; US-PGPUB; POPO; JPO; JPO; JPO; JPO; JPO; JPO; JP					
(value width)	5	0	((address\$3 adj3 extension) near3	USPAT;	2003/05/12 10:38
				US-PGPUB;	
Company   Concatenats   Company			(value width))	,	
0				1	
concatenat\$3) same (address near3 signal)		0	(/	1	0000/05/10 10 00
7	٥	U			2003/05/12 12:20
			concatenates, same (address nears signar)		
The transfer of transfer				l .	
0				l .	
Same ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3)) same (address\$2 EPO, JPO, DERWENT; IBM TOB ((address\$2 adj3 signal) near3 width) same ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3))	7	0	(((address\$2 adj3 signal) near3 width)		2003/05/12 12:25
near3 extension near3 register signal)			same ((address\$2 adj3 extension) near5		
### and (sav\$3 retain\$3 stor\$3)    Combin\$3 concatenat\$3)   Combin\$3 concatenat\$3   Combin\$3 concatenat\$3   Combin\$3 concatenat\$3   Combin\$3 concatenat\$3)   Combin\$3 concatenat\$3   Combin\$3   Combin\$3 concatenat\$3   Combin\$3   Combin\$3					
8					
((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3))					0000/05/10 10 07
Combin\$3 concatenat\$3)	8	U			2003/05/12 12:27
9 376 ((address\$2 adj3 signal) near3 width)					
9 376 ((address\$2 adj3 signal) near3 width)  11 0 (((address\$2 adj3 signal) near3 width)   IBM_TDB   US-PGCPUB; EPO; JPO; DERWENT; IBM_TDB   US-PGPUB; EPO; JPO; DERWENT; IBM_			(00.2.170 00.1000.10070))		
US-PGPUB; EPO; JPO; DERWENT; IBM_TDB  10				1	
Combin\$3 concatenat\$3)   Combin\$3 concatenat\$3   Combin\$3   Combin\$3 concatenat\$3   Combin\$3 concatenat\$3   Combin\$3   Combin\$3 concatenat\$3   Combin\$3   Com	9	376	((address\$2 adj3 signal) near3 width)	USPĀT;	2003/05/12 12:27
DERWENT;   IBM TDB   USPĀT;   US-PGPUB;   EPO; JPO;   DERWENT;   IBM TDB   USPĀT;   US-PCPUB;   EPO; JPO;   DERWENT;   IBM TDB   USPĀT;   US-PCPUB;   EPO; JPO;   DERWENT;   IBM TDB   EPO; JPO;   EPO; JP					
IBM_TDB USPAT; U					
11 0 (((address\$2 adj3 signal) near3 width) ) and (((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3)) ) USPĀT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; DEWENT; IBM TDB USPĀT; US-PGPUB; EPO; JPO; DEWENT; IBM TDB				r -	
and (((address\$2 adj3 extension) near5	11	٥	(((address\$2 adi3 signal) near3 width) \		2003/05/12 12:28
(combin\$3 concatenat\$3))   EPO; JPO; DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB USPAT; USPAT; USPAT; USPAGPUB; EPO; JPO; DERWENT; IBM_TDB USPAGPUB; LEVIC	**	ı ı			2003/03/12 12.20
10 10 ((address\$2 adj3 extension) near5 (combin\$3 concatenat\$3))  - 36 (address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))  - 36 ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 extension) (address adj3 extension) (address adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 extension) (address adj3 extension) (address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$\$3 invox\$\$3					
10			(		
(combin\$3 concatenat\$3))  (combin\$3 concatenat\$3))  (daddress adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))  (daddress adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3				IBM_TDB	
- 36 (address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value)) ((address adj3 extension) adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3	10	10			2003/05/12 12:28
- 36 (address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))  - 36 ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3			(combin\$3 concatenat\$3))		
- 36 (address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))  - 36 ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3					
- 36 (address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))  - 36 ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3					
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record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value )) ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3		55			
(address adj3 value))) and (switch\$3 (thread\$3 execution process processes value)) ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3					
value ))  ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3			(address adj3 value))) and (switch\$3		
- 36 ((address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3			• •	IBM_TDB	
adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3		2.5		HCDAM.	2002/05/00 10:55
record\$3) near3 ((address adj3 extension) (address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3	-	36			2003/03/09 10:56
(address adj3 value))) and (switch\$3 (thread\$3 execution process processes value))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3				1	
(thread\$3 execution process processes value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3					
value ))) and ((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3					
pci (peripheral adj2 component adj2 interconnect) device address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3	Į			_ '	
adj2 signal) width (computing adj2 device) concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3	1				
concatenat\$3 register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3		Ī			
processor os o/s (operating adj2 system) (address\$3 adj3 rang\$3) call\$3 invok\$3	ļ				
(address\$3 adj3 rang\$3) call\$3 invok\$3		ļ			
			function\$3 initial\$7 restor\$3 writ\$3		
read\$3 interrupt\$3 value suspend\$3					
resum\$3)					

-	32	(address adj3 extension) and (extension adj3 register) and ((sav\$3 keep\$3 stor\$3	USPAT;	2003/05/09	10:56
		record\$3) near5 ((address adj3 extension)	US-PGPUB; EPO; JPO;		
		(address adj3 value))) and ((interrupt\$3	DERWENT;		
		chang\$3 swap\$3 switch\$3) near5 (thread\$3	IBM TDB		
		execution process processes value))	_		
-	32	, , , , , , , , , , , , , , , , , , , ,	USPAT;	2003/05/09	12:32
		adj3 register) and ((sav\$3 keep\$3 stor\$3	US-PGPUB;		
		record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3	EPO; JPO;		
		chang\$3 swap\$3 switch\$3) near5 (thread\$3	DERWENT; IBM TDB		
		execution process processes value))) and	1511_105		
		((peripheral adj2 device) pci (peripheral			
		adj2 component adj2 interconnect) device			
		address\$3 (address\$3 adj2 signal) width			
		(computing adj2 device) concatenat\$3			
		register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3			
		adj3 rang\$3) call\$3 invok\$3 function\$3			
		initial\$7 restor\$3 writ\$3 read\$3			
İ		<pre>interrupt\$3 value suspend\$3 resum\$3)</pre>	}		
-	26	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	USPAT;	2003/05/09	12:44
		adj3 register) and ((sav\$3 keep\$3 stor\$3	US-PGPUB;		
		record\$3) near5 ((address adj3 extension) (address adj3 value))) and ((interrupt\$3	EPO; JPO;		
	1	chang\$3 swap\$3 switch\$3) near5 (thread\$3	DERWENT; IBM TDB		
		execution process processes value))) and	120.1.100		
		((peripheral adj2 device) pci (peripheral			
		adj2 component adj2 interconnect) device			
		address\$3 (address\$3 adj2 signal) width			
		(computing adj2 device) concatenat\$3			
		register\$3 concurrent\$3 cpu processor os o/s (operating adj2 system) (address\$3			
		adj3 rang\$3) call\$3 invok\$3 function\$3			
		initial\$7 restor\$3 writ\$3 read\$3			
		<pre>interrupt\$3 value suspend\$3 resum\$3) ) and</pre>			
		((local data pci) near3 bus\$2)			
-	0	(((address adj3 extension) and (extension	USPAT;	2003/05/09 1	12:45
		<pre>adj3 register) and ((sav\$3 keep\$3 stor\$3 record\$3) near5 ((address adj3 extension)</pre>	US-PGPUB; EPO; JPO;		
		(address adj3 value))) and ((interrupt\$3	DERWENT;		
		chang\$3 swap\$3 switch\$3) near5 (thread\$3	IBM TDB		
		execution process processes value))) and	_		
		((peripheral adj2 device) pci (peripheral			
		adj2 component adj2 interconnect) device			
		address\$3 (address\$3 adj2 signal) width (computing adj2 device) concatenat\$3			
		register\$3 concurrent\$3 cpu processor os			
		o/s (operating adj2 system) (address\$3			
		adj3 rang\$3) call\$3 invok\$3 function\$3			
		initial\$7 restor\$3 writ\$3 read\$3			
		<pre>interrupt\$3 value suspend\$3 resum\$3) ) and (pci near3 (device bus\$2))</pre>			
_	ا م	(((address adj3 extension) and (extension)	USPAT;	2003/05/09 1	2 - 4 2
		adj3 register) and ((sav\$3 keep\$3 stor\$3	US-PGPUB;	2003,03,03	
1		record\$3) near5 ((address adj3 extension)	EPO; JPO;		
		(address adj3 value))) and ((interrupt\$3	DERWENT;		
		chang\$3 swap\$3 switch\$3) near5 (thread\$3	IBM_TDB		
		execution process processes value))) and			
		((peripheral adj2 device) pci (peripheral adj2 component adj2 interconnect) device			
		adjz component adjz interconnect; device address\$3 (address\$3 adj2 signal) width			
		(computing adj2 device) concatenat\$3			
1		register\$3 concurrent\$3 cpu processor os			
		o/s (operating adj2 system) (address\$3			
		adj3 rang\$3) call\$3 invok\$3 function\$3			
		<pre>initial\$7 restor\$3 writ\$3 read\$3 interrupt\$3 value suspend\$3 resum\$3) ) and</pre>	į		
		(pci near7 (device bus\$2))			
	L	· · · · · · · · · · · · · · · · · · ·		L	

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[ <del>-</del>	3	(((address adj3 extension) and (extension	USPAT;	2003/05/09 12:49
		adj3 register) and ((sav\$3 keep\$3 stor\$3	US-PGPUB;	2003/03/03 12:43
		record\$3) near5 ((address adj3 extension)	EPO; JPO;	
		(address adj3 value))) and ((interrupt\$3	DERWENT;	
		chang\$3 swap\$3 switch\$3) near5 (thread\$3	IBM TDB	
		execution process processes value))) and		
		((peripheral adj2 device) pci (peripheral		
		adj2 component adj2 interconnect) device		
		address\$3 (address\$3 adj2 signal) width		
		(computing adj2 device) concatenat\$3		
		register\$3 concurrent\$3 cpu processor os		
		o/s (operating adj2 system) (address\$3		
		adj3 rang\$3) call\$3 invok\$3 function\$3		
ļ		initial\$7 restor\$3 writ\$3 read\$3		
		interrupt\$3 value suspend\$3 resum\$3) ) and		
		(peripheral adj3 device)		
-	4	("6292820" "6289369") .pn.	USPAT;	2003/05/09 13:41
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	

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